class mem\_test extends uvm\_test;

`uvm\_component\_utils(mem\_test) //factory registration

mem\_env env;

mem\_sequence seq;

function new(string name = "mem\_test",uvm\_component parent);

super.new(name,parent);

endfunction

virtual function void build\_phase(uvm\_phase phase);

super.build\_phase(phase);

env=mem\_env::type\_id::create("env",this);

seq=mem\_sequence::type\_id::create("seq");

endfunction

virtual task run\_phase(uvm\_phase phase);

super.run\_phase(phase);

phase.raise\_objection(this);

seq.start(env.agen.seqr);

#100;

phase.drop\_objection(this);

`uvm\_info("id","---------------TEST----------------",UVM\_MEDIUM)

endtask

endclass